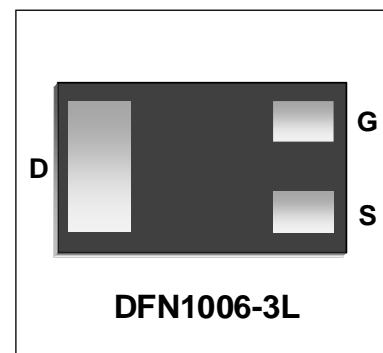


Features

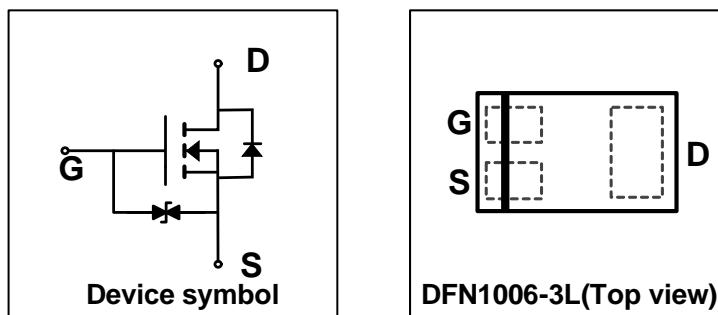
- Way-on Small Signal MOSFETs
- $V_{DS} = 30V$, $I_D = 0.93A$
- $R_{DS(on)} < 0.5\Omega$ @ $V_{GS} = 4.5V$
- $R_{DS(on)} < 0.6\Omega$ @ $V_{GS} = 2.5V$
- Trench LV MOSFET Technology
- ESD Protected



Mechanical Characteristics

- DFN1006-3L Package
- Marking : Making Code
- RoHS Compliant

Schematic & PIN Configuration



Absolute Maximum Rating ($T_A=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 12	V
Continuous Drain Current $T_A = 25^\circ C$	I_D	0.93	A
Pulsed Drain Current ¹	I_{DM}	3.7	A
Power Dissipation $T_A = 25^\circ C$	P_D	715	mW
Operating Junction and Storage Temperature Range	T_J , T_{STG}	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction to Ambient ²	$R_{\theta JA}$	305	°C/W

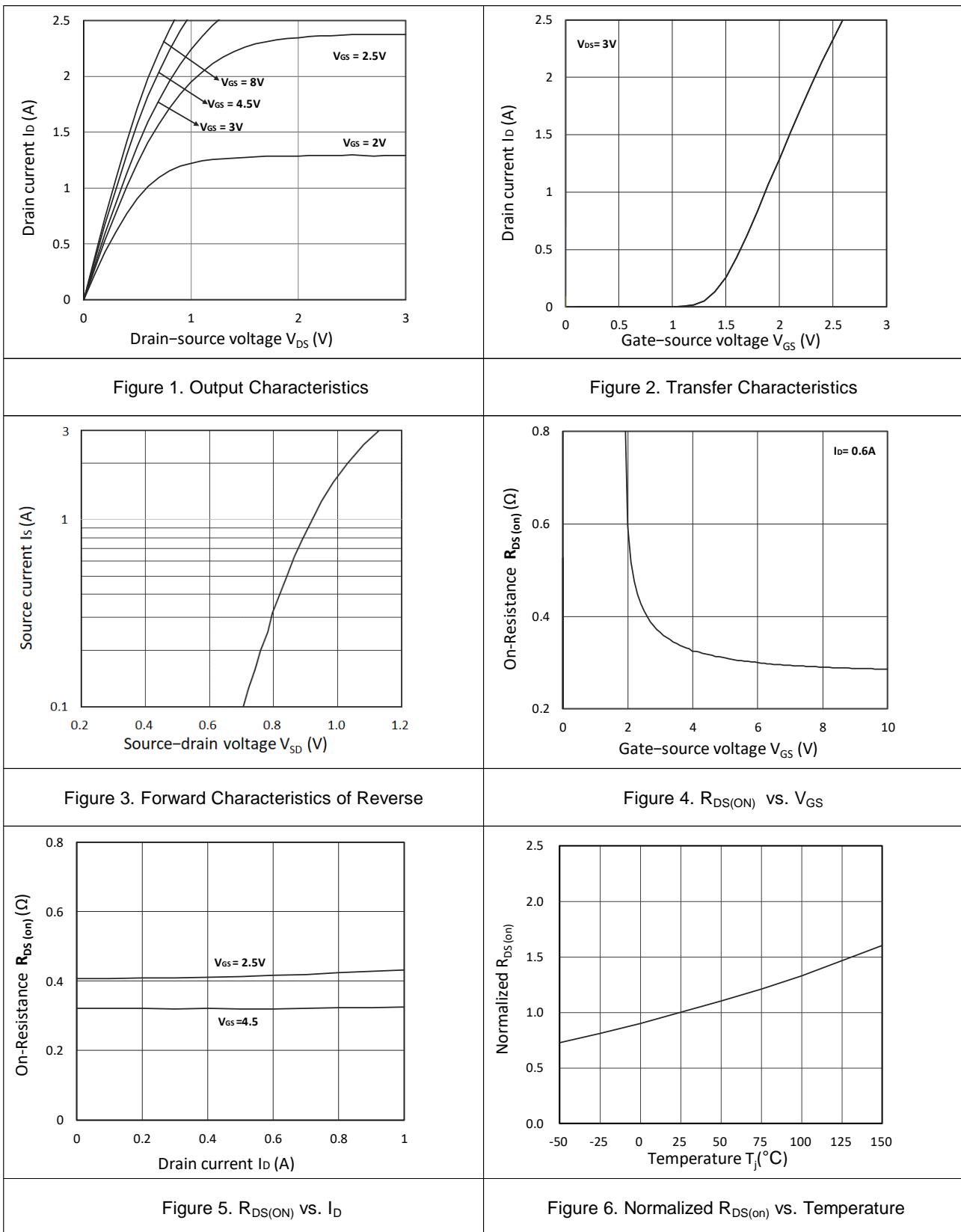
Electrical Characteristics (T_J=25°C unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D = 250μA	30	-	-	V
Gate leakage Current	I _{GSS}	V _{GS} =±10 V, V _{DS} =0V	-	-	±10	μA
Drain Cut-off Current	I _{DSS}	V _{DS} =30V, V _{GS} =0V	-	-	1	μA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	0.5	1	1.5	V
Drain-Source on-state Resistance ³	R _{DS(on)}	V _{GS} =4.5V, I _D =0.6A	-	0.32	0.5	Ω
		V _{GS} =2.5V, I _D =0.3A	-	0.41	0.6	
Dynamic characteristics⁴						
Input Capacitance	C _{iss}	V _{DS} = 15V, V _{GS} = 0V, f = 1 MHz	-	37	-	pF
Output Capacitance	C _{oss}		-	8.6	-	
Reverse Transfer Capacitance	C _{rss}		-	5.4	-	
Switching Characteristics⁴						
Total Gate Charge	Q _g	V _{GS} = 4.5V, V _{DS} = 15V, I _D = 0.6A	-	0.65	-	nC
Gate-Source Charge	Q _{gs}		-	0.14	-	
Gate-Drain Charge	Q _{gd}		-	0.18	-	
Turn-on Delay Time	t _{d(on)}	V _{GS} =4.5V, V _{DD} = 15V, R _G = 3Ω, I _D = 0.6A	-	2.1	-	ns
Rise Time	t _r		-	1.2	-	
Turn-off Delay Time	t _{d(off)}		-	6.6	-	
Fall Time	t _f		-	1.3	-	
Source-Drain Body Diode Characteristics						
Diode Forward Voltage ³	V _{SD}	I _S = 0.6A, V _{GS} = 0V	-	-	1.2	V
Continuous Source Current	I _S		-	-	0.93	A

Notes:

1. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C.
2. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
3. Pulse Test: Pulse width≤300μs, duty cycle≤2%.
4. This value is guaranteed by design hence it is not included in the production test.

Typical Characteristics



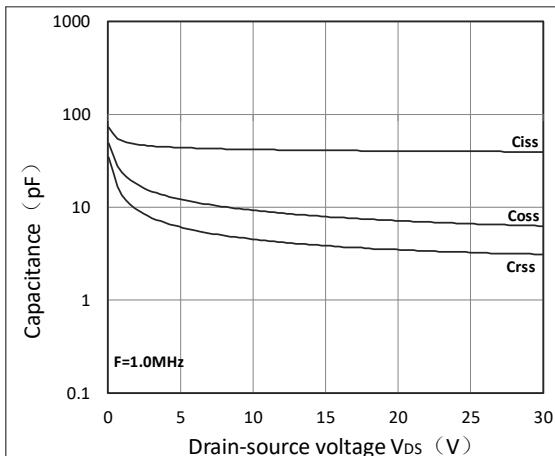


Figure 7. Capacitance Characteristics

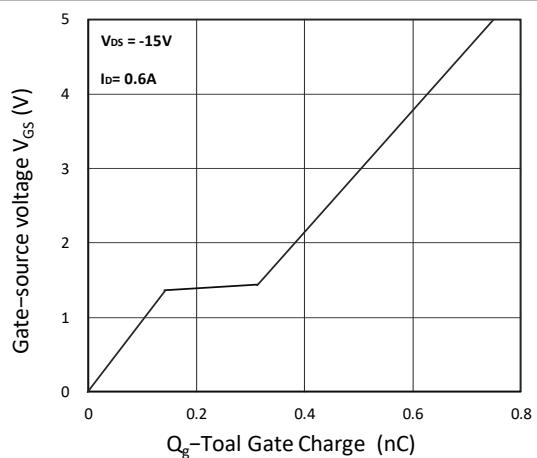
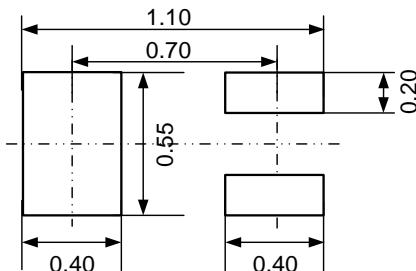


Figure 8. Gate Charge Characteristics

Outline Drawing – DFN1006-3L

PACKAGE OUTLINE		DFN1006-3L			
SYMBOL		MILLIMETER		INCHES	
		MIN.	MAX.	MIN.	MAX.
A		0.45	0.55	0.018	0.022
A1		0.00	0.05	0.000	0.002
b		0.40	0.60	0.016	0.024
b1		0.10	0.20	0.004	0.008
D		0.95	1.05	0.037	0.041
e		0.65BSC		0.026BSC	
E		0.55	0.65	0.022	0.026
E1		0.19BSC		0.007BSC	
L		0.20	0.30	0.008	0.012

Land Pattern



Marking Codes

Part Number	WM03N09F
Marking Code	N5

Package Information

Qty: 10k/Reel

CONTACT INFORMATION.

No.1001, Shiwan (7) Road, Pudong District, Shanghai, P.R.China.201207

Tel: 86-21-68969993 Fax: 86-21-50757680 Email: market@way-on.com

WAYON website: <http://www.way-on.com>

For additional information, please contact your local Sales Representative.

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Specifications are subject to change without notice.

The device characteristics and parameters in this data sheet can and do vary in different applications and actual device performance may vary over time. Users should verify actual device performance in their specific applications.