

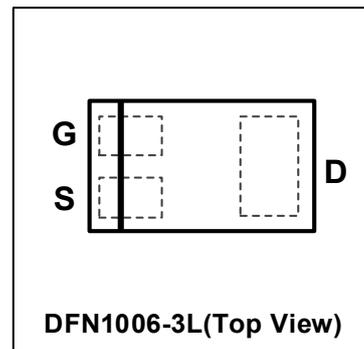
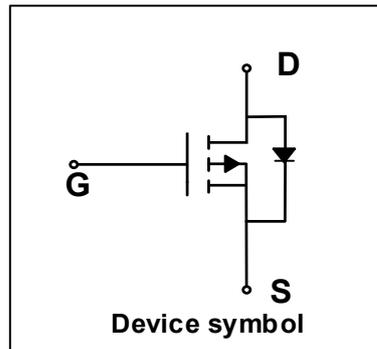
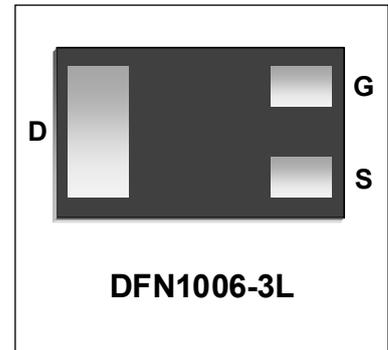
### Features

- Way-on Small Signal MOSFETs
- $V_{DS} = -20V$ ,  $I_D = -1.8A$   
 $R_{DS(on)} < 135m\Omega @ V_{GS} = -4.5V$   
 $R_{DS(on)} < 180m\Omega @ V_{GS} = -2.5V$
- Trench LV MOSFET Technology

### Mechanical Characteristics

- DFN1006-3L Package
- Marking : Making Code
- RoHS Compliant

### Schematic & PIN Configuration



### Absolute Maximum Rating ( $T_A=25^\circ C$ unless otherwise noted)

Rating	Symbol	Value	Units
Drain-Source Voltage	$V_{DS}$	-20	V
Gate-Source Voltage	$V_{GS}$	$\pm 10$	V
Continuous Drain Current	$I_D$	-1.8	A
Pulsed Drain Current <sup>1</sup>	$I_{DM}$	-7.2	A
Power Dissipation	$P_D$	0.7	W
Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 to 150	$^\circ C$

### Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction to Ambient <sup>2</sup>	$R_{\theta JA}$	179	$^\circ C/W$

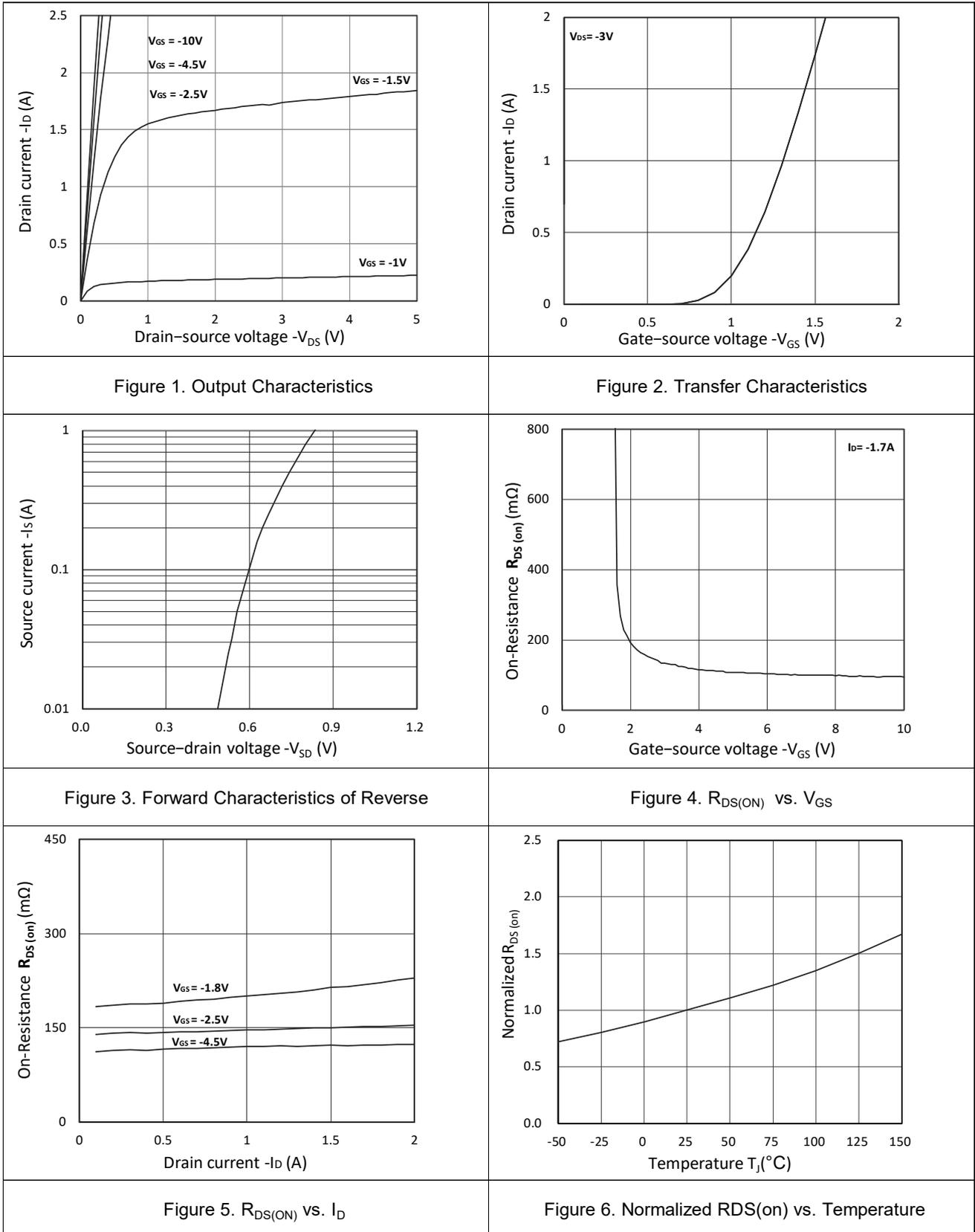
**Electrical Characteristics** ( $T_J=25^{\circ}\text{C}$  unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=-250\mu A$	-20	-	-	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=-20V, V_{GS}=0V$	-	-	-1	$\mu A$
Gate-Source Leakage	$I_{GSS}$	$V_{DS}=0V, V_{GS}=\pm 10V$	-	-	$\pm 100$	nA
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-0.4	-	-1.0	V
Drain-Source on-State Resistance <sup>3</sup>	$R_{DS(on)}$	$V_{GS}=-4.5V, I_D=-1.7A$	-	112	135	m $\Omega$
		$V_{GS}=-2.5V, I_D=-1.2A$	-	149	180	
		$V_{GS}=-1.8V, I_D=-1.0A$	-	207	250	
<b>Dynamic Characteristics<sup>4</sup></b>						
Input Capacitance	$C_{iss}$	$V_{GS}=0V, V_{DS}=-10V,$ $f=1\text{MHz}$	-	168	-	pF
Output Capacitance	$C_{oss}$		-	30	-	
Reverse Transfer Capacitance	$C_{rss}$		-	24	-	
<b>Switching Characteristics<sup>4</sup></b>						
Total Gate Charge	$Q_g$	$V_{GS}=-4.5V, V_{DS}=-10V,$ $I_D=-1.7A$		2.72		nC
Gate-Source Charge	$Q_{gs}$			0.49		
Gate-Drain Charge	$Q_{gd}$			0.57		
Turn-On Delay Time	$t_{d(on)}$	$V_{GS}=-4.5V, V_{DD}=-10V,$ $R_G=3\Omega, I_D=-1.7A$	-	6.5	-	ns
Rise Time	$t_r$		-	4.8	-	
Turn-Off Delay Time	$t_{d(off)}$		-	18	-	
Fall Time	$t_f$		-	5	-	
<b>Drain-Source Body Diode Characteristics</b>						
Diode Forward Voltage <sup>3</sup>	$V_{SD}$	$I_S=-1A, V_{GS}=0V$	-	-	-1.2	V
Continuous Source Current	$I_S$	-	-	-	-1.8	A

**Notes:**

1. Repetitive rating, pulse width limited by junction temperature  $T_{J(MAX)}=150^{\circ}\text{C}$ .
2. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
3. Pulse Test: Pulse width $\leq 300\mu s$ , duty cycle $\leq 2\%$ .
4. This value is guaranteed by design hence it is not included in the production test.

### Typical Characteristics



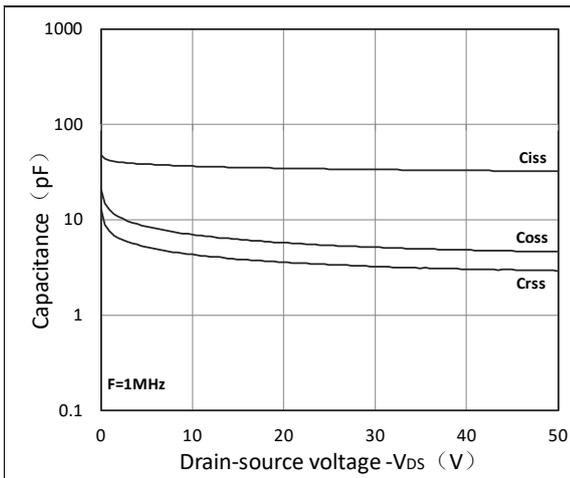


Figure 7. Capacitance Characteristics

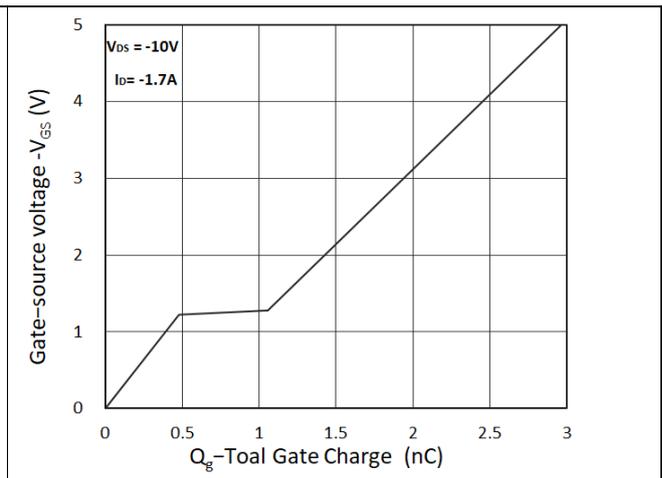
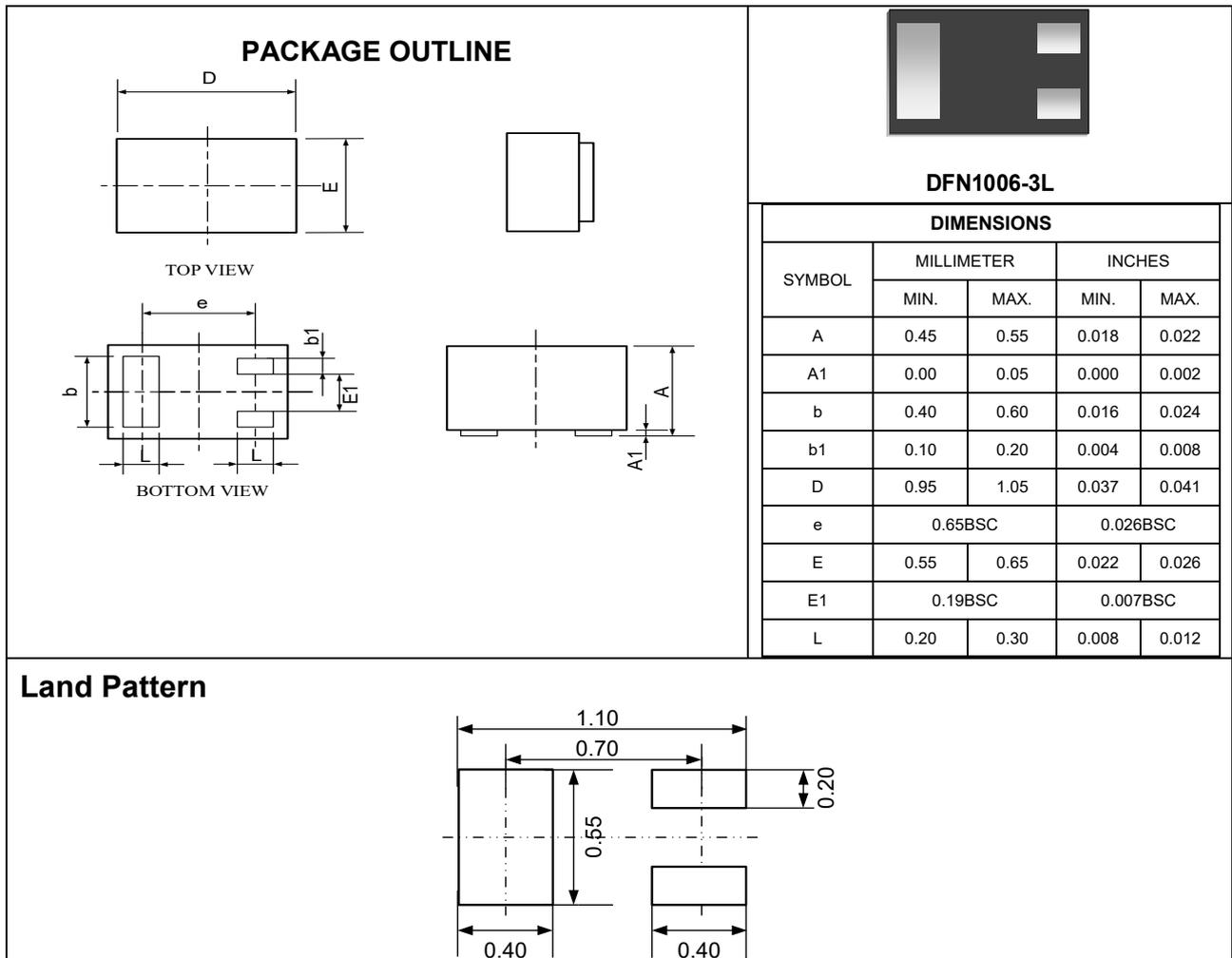


Figure 8. Gate Charge Characteristics

## Outline Drawing – DFN1006-3L



## Marking Codes

Part Number	WM02P18F
Marking Code	

## Package Information

Qty: 10k/Reel

## CONTACT INFORMATION

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Specifications are subject to change without notice.  
The device characteristics and parameters in this data sheet can and do vary in different applications and actual device performance may vary over time.  
Users should verify actual device performance in their specific applications.