



## Description

### JMT N-channel Enhancement Mode Power MOSFET

#### Features

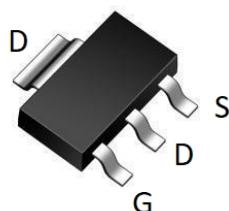
- 100V, 5A
- $R_{DS(ON)} < 125\text{m}\Omega$  @  $V_{GS} = 10\text{V}$
- $R_{DS(ON)} < 147\text{m}\Omega$  @  $V_{GS} = 4.5\text{V}$
- Advanced Trench Technology
- Excellent  $R_{DS(ON)}$  and Low Gate Charge
- Lead free product is acquired

#### Application

- Load Switch
- PWM Application
- Power management



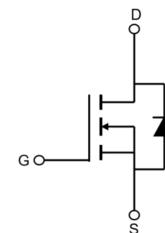
100% UIS TESTED!  
100%  $\Delta V_{ds}$  TESTED!



SOT-223 top view



Marking and pin Assignment



Schematic Diagram

## Package Marking and Ordering Information

Device Marking	Device	OUTLINE	Device Package	Reel Size	Reel (PCS)	Per Carton (PCS)
JMTY11DN10A	JMTY11DN10A	TAPING	SOT-223	13inch	4000	40000

## Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter		Max.	Units
$V_{DSS}$	Drain-Source Voltage		100	V
$V_{GSS}$	Gate-Source Voltage		$\pm 20$	V
$I_D$	Continuous Drain Current	$T_A = 25^\circ\text{C}$	5	A
		$T_A = 100^\circ\text{C}$	3.3	A
$I_{DM}$	Pulsed Drain Current <sup>note1</sup>		20	A
EAS	Single Pulsed Avalanche Energy <sup>note2</sup>		4	mJ
$P_D$	Power Dissipation	$T_A = 25^\circ\text{C}$	5	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient		25	$^\circ\text{C}/\text{W}$
$T_J, T_{STG}$	Operating and Storage Temperature Range		-55 to +150	$^\circ\text{C}$

**Electrical Characteristics** ( $T_J=25^\circ\text{C}$  unless otherwise specified)

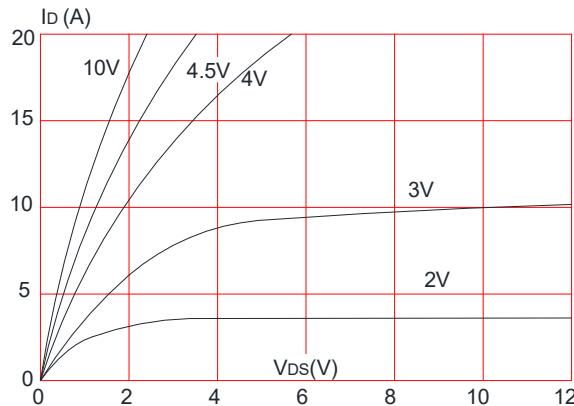
Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
<b>Off Characteristic</b>						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=250\mu\text{A}$	100	-	-	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{DS}=100\text{V}$ , $V_{GS}=0\text{V}$ ,	-	-	1.0	$\mu\text{A}$
$I_{GSS}$	Gate to Body Leakage Current	$V_{DS}=0\text{V}$ , $V_{GS}=\pm 20\text{V}$	-	-	$\pm 100$	nA
<b>On Characteristics</b>						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$ , $I_D=250\mu\text{A}$	1.0	-	2.5	V
$R_{DS(\text{on})}$ note3	Static Drain-Source on-Resistance	$V_{GS}=10\text{V}$ , $I_D=5\text{A}$	-	96	125	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}$ , $I_D=3\text{A}$	-	105	147	$\text{m}\Omega$
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS}=25\text{V}$ , $V_{GS}=0\text{V}$ , $f=1.0\text{MHz}$	-	610	-	pF
$C_{oss}$	Output Capacitance		-	40	-	pF
$C_{rss}$	Reverse Transfer Capacitance		-	25	-	pF
$Q_g$	Total Gate Charge	$V_{DS}=50\text{V}$ , $I_D=2\text{A}$ , $V_{GS}=10\text{V}$	-	12	-	nC
$Q_{gs}$	Gate-Source Charge		-	2.2	-	nC
$Q_{gd}$	Gate-Drain("Miller") Charge		-	2.5	-	nC
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-on Delay Time	$V_{DS}=50\text{V}$ , $I_D=3\text{A}$ , $R_G=1.8\Omega$ , $V_{GS}=10\text{V}$	-	7	-	ns
$t_r$	Turn-on Rise Time		-	5	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	16	-	ns
$t_f$	Turn-off Fall Time		-	6	-	ns
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
$I_s$	Maximum Continuous Drain to Source Diode Forward Current	-	-	5	A	
$I_{SM}$	Maximum Pulsed Drain to Source Diode Forward Current	-	-	20	A	
$V_{SD}$	Drain to Source Diode Forward Voltage	$V_{GS}=0\text{V}$ , $I_s=5\text{A}$	-	-	1.2	V
$trr$	Body Diode Reverse Recovery Time	$I_F=3\text{A}$ , $dI/dt=100\text{A}/\mu\text{s}$	-	21	-	ns
$Qrr$	Body Diode Reverse Recovery Charge		-	21	-	nC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

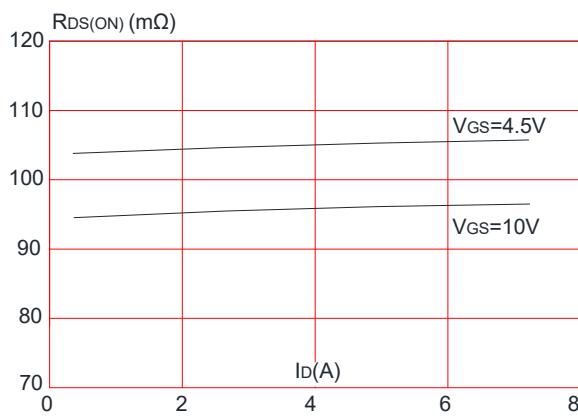
2. EAS condition :  $T_J=25^\circ\text{C}$ ,  $V_{DD}=50\text{V}$ ,  $V_G=10\text{V}$ ,  $L=0.5\text{mH}$ ,  $R_g=25\Omega$ ,  $I_{AS}=4\text{A}$ 3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$ , Duty Cycle $\leq 0.5\%$

## Typical Performance Characteristics

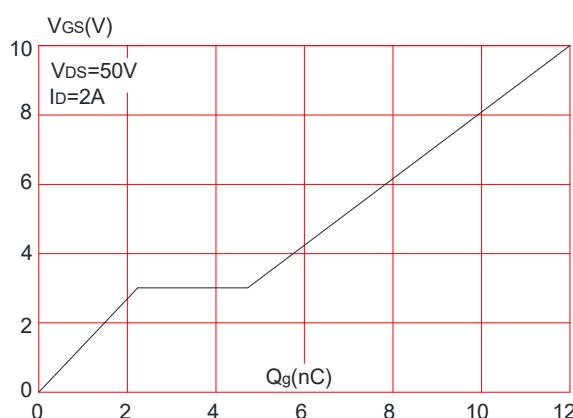
**Figure 1:** Output Characteristics



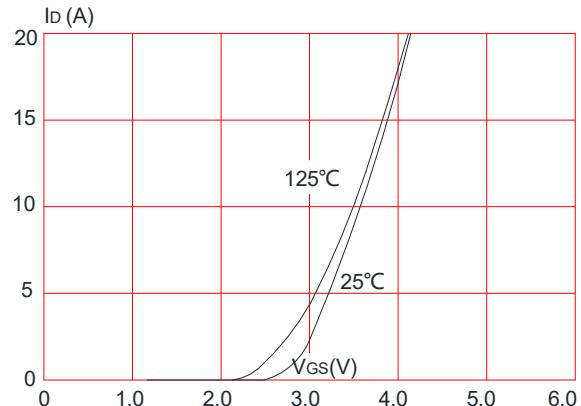
**Figure 3:** On-resistance vs. Drain Current



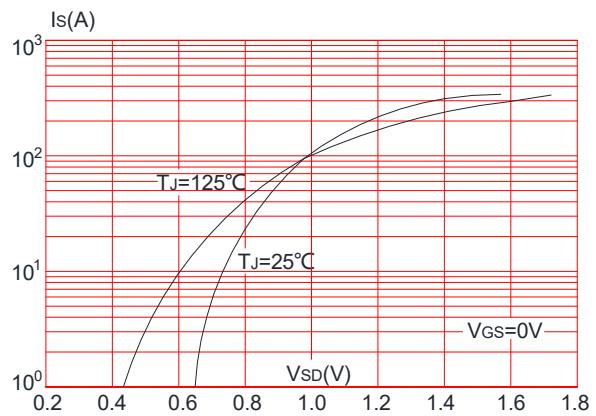
**Figure 5:** Gate Charge Characteristics



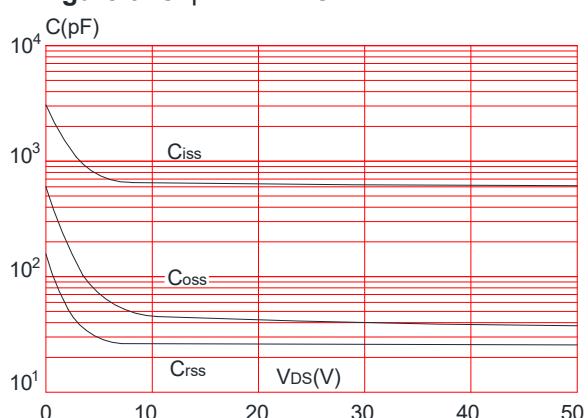
**Figure 2:** Typical Transfer Characteristics



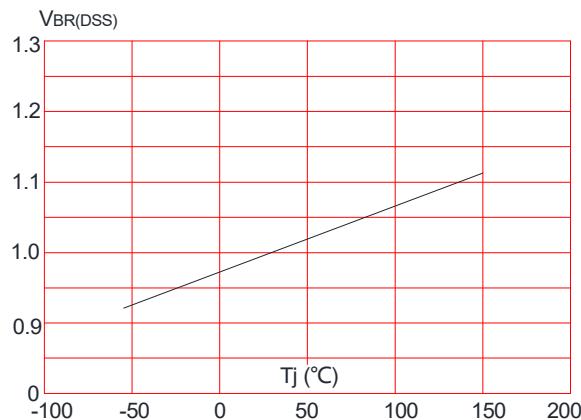
**Figure 4:** Body Diode Characteristics



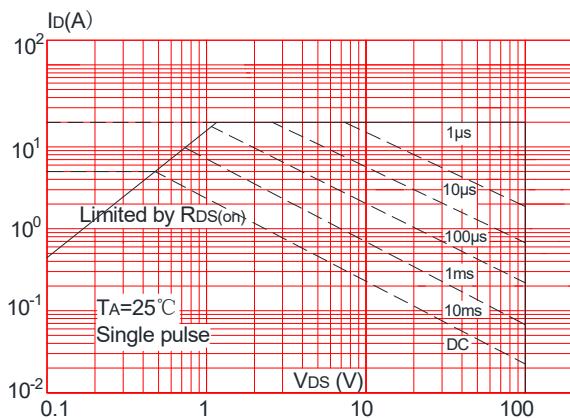
**Figure 6:** Capacitance Characteristics



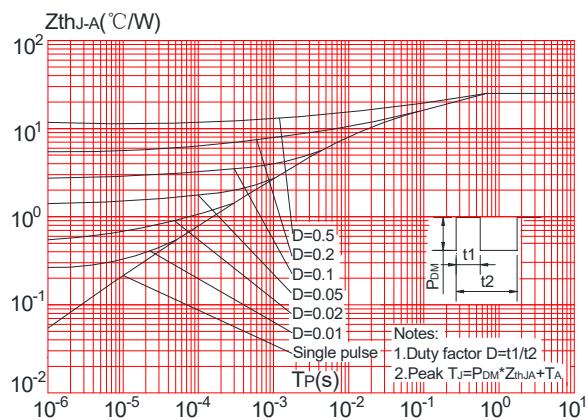
**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



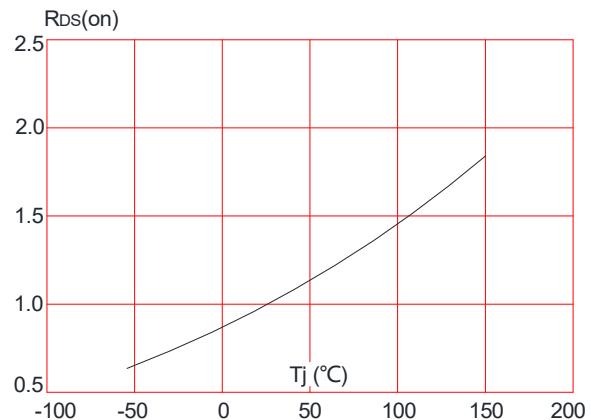
**Figure 9:** Maximum Safe Operating Area



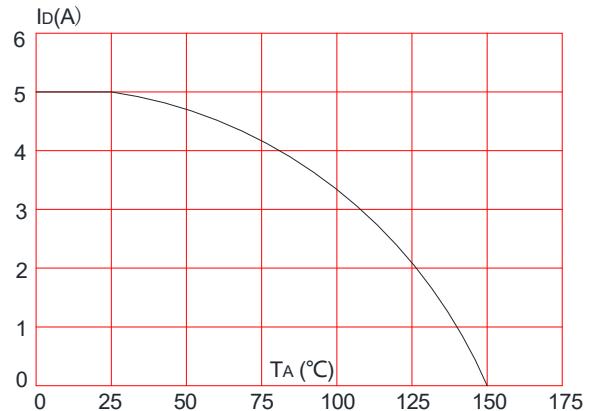
**Figure 11:** Maximum Effective Transient Thermal Impedance, Junction-to-Ambient



**Figure 8:** Normalized on Resistance vs. Junction Temperature



**Figure 10:** Maximum Continuous Drain Current vs. Ambient Temperature



## Test Circuit

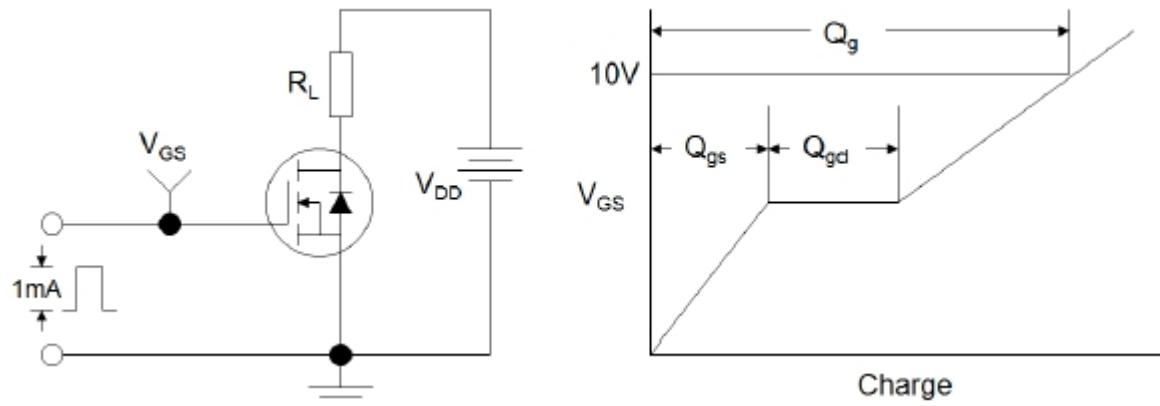


Figure1:Gate Charge Test Circuit & Waveform

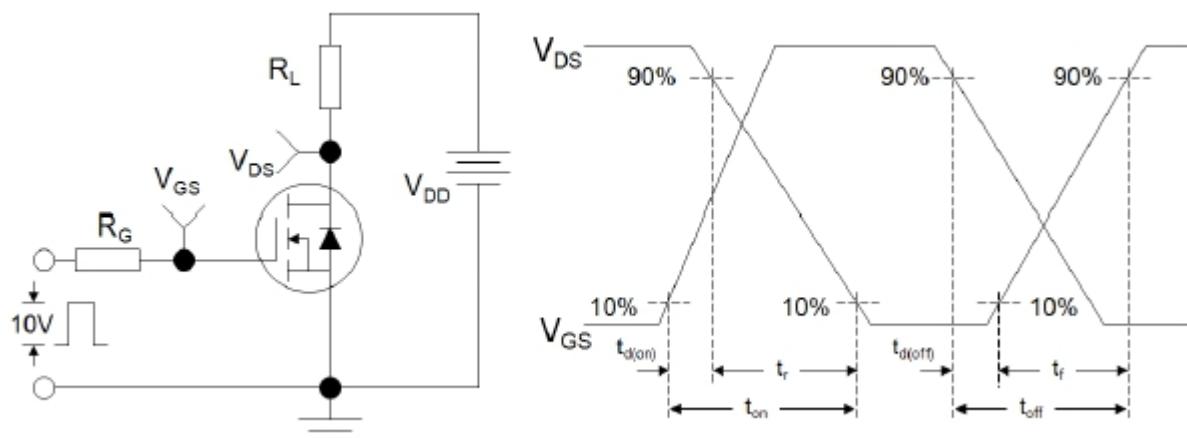


Figure 2: Resistive Switching Test Circuit & Waveforms

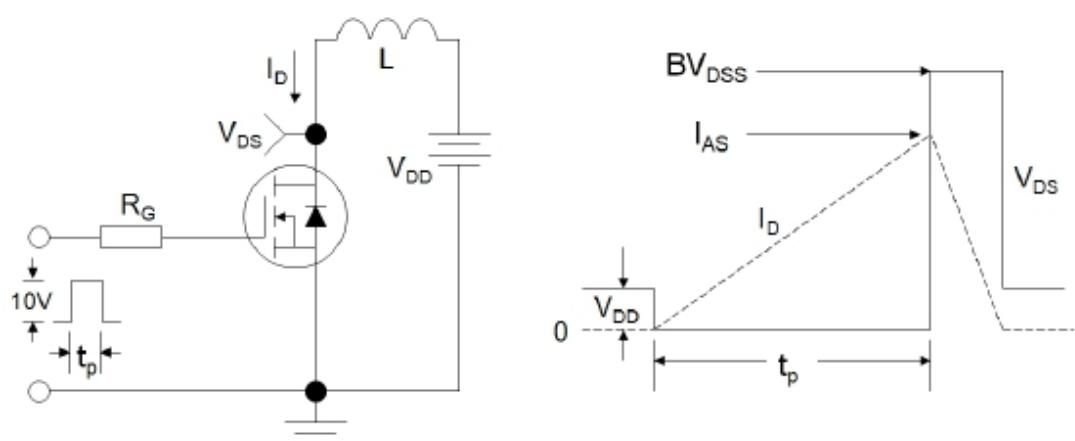
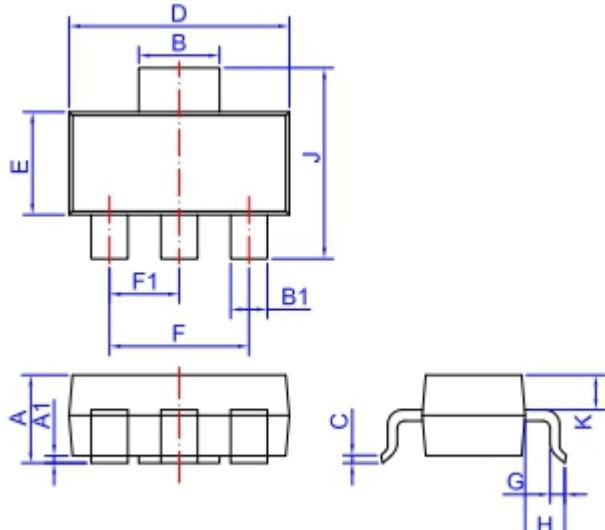


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms



## Package Mechanical Data-SOT-223



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	1.5	1.6	1.8	0.059	0.063	0.071
A1	0.01	0.06	0.10	0.001	0.002	0.004
B	2.9	3.0	3.1	0.114	0.118	0.122
B1	0.6	0.7	0.8	0.024	0.028	0.031
C	0.22	0.26	0.32	0.009	0.010	0.013
D	6.3	6.5	6.7	0.248	0.256	0.264
E	3.3	3.5	3.7	0.130	0.138	0.146
F		4.6			0.181	
F1		2.3			0.091	
G	0.7	0.9	1.1	0.028	0.035	0.043
H	1.5	1.75	2.0	0.059	0.069	0.079
J	6.7	7.0	7.3	0.264	0.276	0.287
K	0.8	0.9	1.0	0.031	0.035	0.039

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