



**-60V 40mΩ P-Ch Power MOSFET**

**Features**

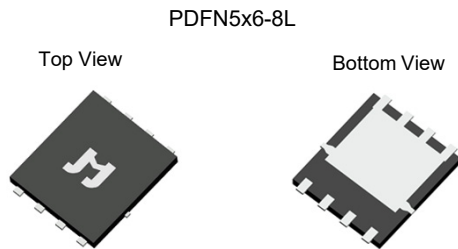
- Low Gate Charge
- 100% UIS Tested, 100% R<sub>g</sub> Tested
- Pb-free Lead Plating
- Halogen-free and RoHS-compliant

**Product Summary**

Parameter	Value	Unit
V <sub>DS</sub>	-60	V
V <sub>GS(th)_Typ</sub>	-2.0	V
I <sub>D</sub> (@ V <sub>GS</sub> = -10V) <sup>(1)</sup>	-24	A
R <sub>DS(ON)_Typ</sub> (@ V <sub>GS</sub> = -10V)	40	mΩ
R <sub>DS(ON)_Typ</sub> (@ V <sub>GS</sub> = -4.5V)	55	mΩ

**Applications**

- LED Back-lighting Application
- DC/DC Power Management
- High Side Switch for Full Bridge Converter

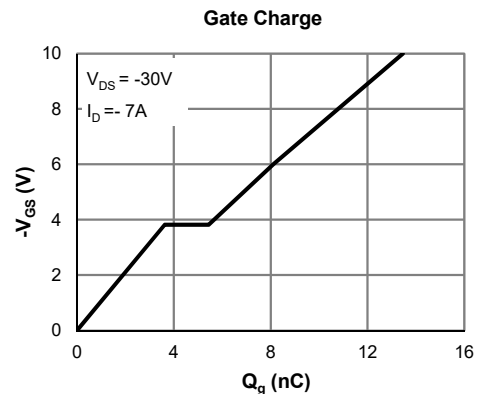
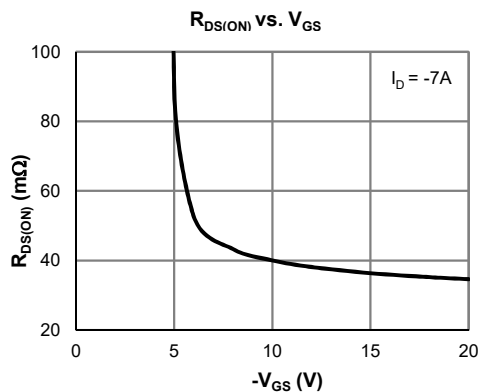


**Ordering Information**

Device	Package	# of Pins	Marking	MSL	T <sub>J</sub> (°C)	Media	Quantity (pcs)
JMPL0648AG-13	PDFN5x6-8L	8	PL0648A	1	-55 to 150	13-inch Reel	5000

**Absolute Maximum Ratings** (@ T<sub>A</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	V <sub>DS</sub>	-60	V
Gate-to-Source Voltage	V <sub>GS</sub>	±20	V
Continuous Drain Current <sup>(1)</sup>	I <sub>D</sub>	T <sub>C</sub> = 25°C	-24
		T <sub>C</sub> = 100°C	-15
Pulsed Drain Current <sup>(2)</sup>	I <sub>DM</sub>	-96	A
Avalanche Energy <sup>(3)</sup>	E <sub>AS</sub>	75	mJ
Power Dissipation <sup>(4)</sup>	P <sub>D</sub>	T <sub>C</sub> = 25°C	50
		T <sub>C</sub> = 100°C	20
Junction & Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to 150	°C





**Electrical Characteristics** (@  $T_J = 25^\circ\text{C}$  unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
<b>STATIC PARAMETERS</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = -250\mu\text{A}, V_{GS} = 0\text{V}$	-60			V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -48\text{V}, V_{GS} = 0\text{V}$ $T_J = 55^\circ\text{C}$			-1.0 -5.0	$\mu\text{A}$
Gate-Body Leakage Current	$I_{GSS}$	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$			$\pm 100$	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\mu\text{A}$	-1.0	-2.0	-3.0	V
Static Drain-Source ON-Resistance	$R_{DS(on)}$	$V_{GS} = -10\text{V}, I_D = -7\text{A}$		40	50	$\text{m}\Omega$
		$V_{GS} = -4.5\text{V}, I_D = -5\text{A}$		55	72	$\text{m}\Omega$
Forward Transconductance	$g_{FS}$	$V_{DS} = -5\text{V}, I_D = -7\text{A}$		20		S
Diode Forward Voltage	$V_{SD}$	$I_S = -1\text{A}, V_{GS} = 0\text{V}$		-0.77	-1.0	V
Diode Continuous Current	$I_S$	$T_C = 25^\circ\text{C}$			-24	A

**DYNAMIC PARAMETERS** <sup>(5)</sup>

Input Capacitance	$C_{iss}$	$V_{GS} = 0\text{V}, V_{DS} = -30\text{V}, f = 1\text{MHz}$		855		pF
Output Capacitance	$C_{oss}$			189		pF
Reverse Transfer Capacitance	$C_{rss}$			9.5		pF
Gate Resistance	$R_g$	$V_{GS} = 0\text{V}, V_{DS} = 0\text{V}, f = 1\text{MHz}$		9.8		$\Omega$

**SWITCHING PARAMETERS** <sup>(5)</sup>

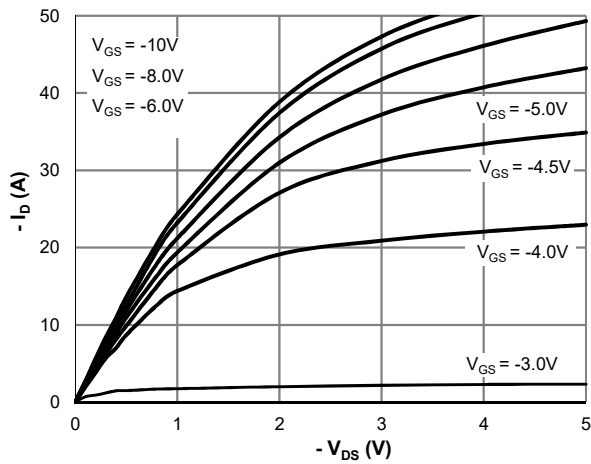
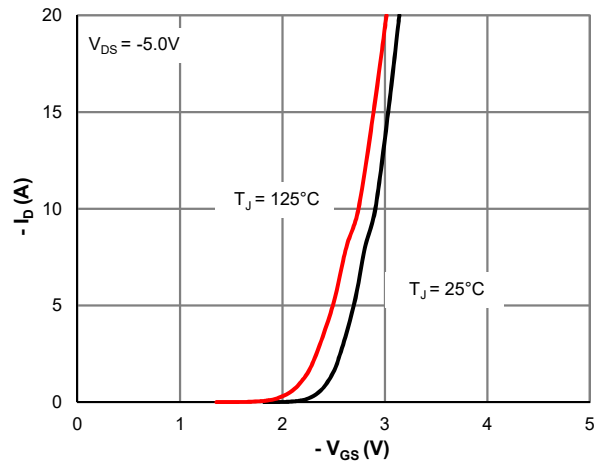
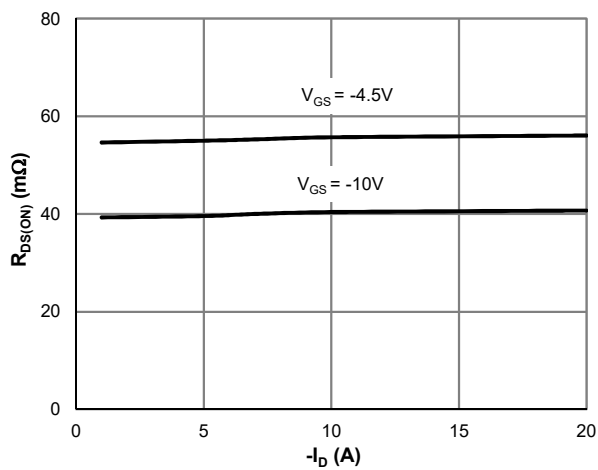
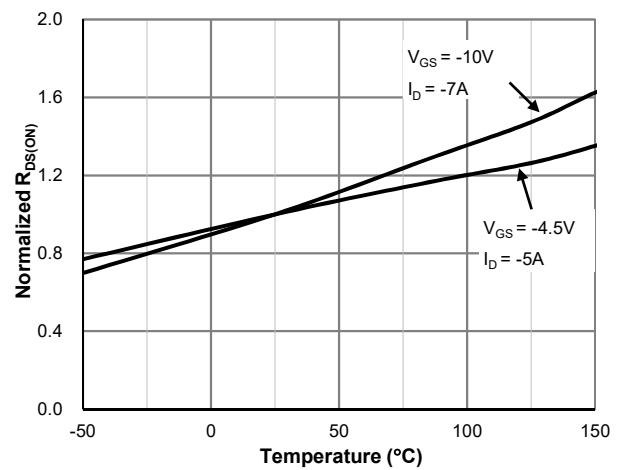
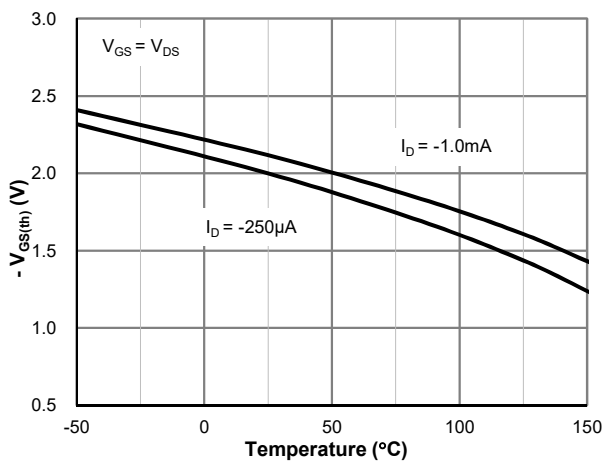
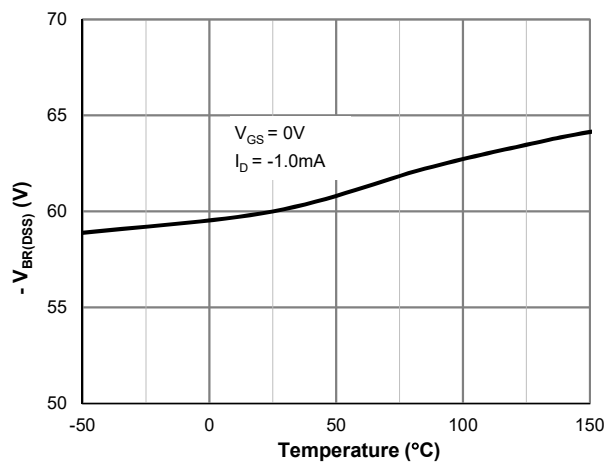
Total Gate Charge (@ $V_{GS} = -10\text{V}$ )	$Q_g$	$V_{GS} = 0 \text{ to } -10\text{V}$ $V_{DS} = -30\text{V}, I_D = -7\text{A}$		13.5		nC
Total Gate Charge (@ $V_{GS} = -4.5\text{V}$ )	$Q_g$			6.4		nC
Gate Source Charge	$Q_{gs}$			3.6		nC
Gate Drain Charge	$Q_{gd}$			1.8		nC
Turn-On Delay Time	$t_{D(on)}$	$V_{GS} = -10\text{V}, V_{DS} = -30\text{V}$ $R_L = 4.3\Omega, R_{GEN} = 3\Omega$		5.4		ns
Turn-On Rise Time	$t_r$			1.9		ns
Turn-Off Delay Time	$t_{D(off)}$			23		ns
Turn-Off Fall Time	$t_f$			4.2		ns
Body Diode Reverse Recovery Time	$t_{rr}$		$I_F = -7\text{A}, dI_F/dt = -100\text{A}/\mu\text{s}$		27	
Body Diode Reverse Recovery Charge	$Q_{rr}$	$I_F = -7\text{A}, dI_F/dt = -100\text{A}/\mu\text{s}$		28		nC

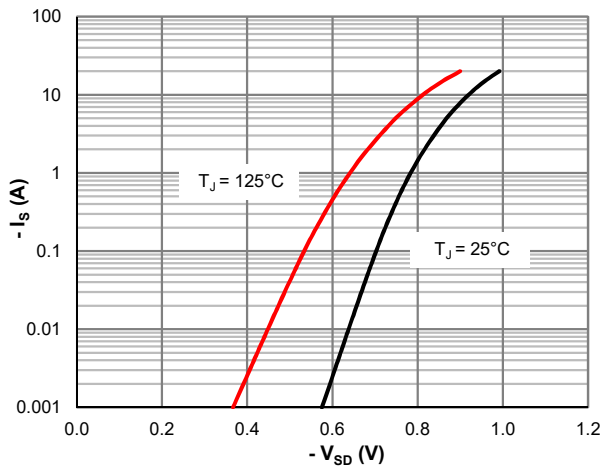
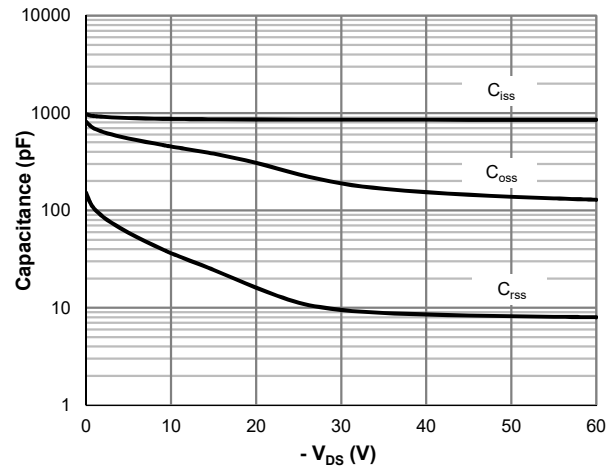
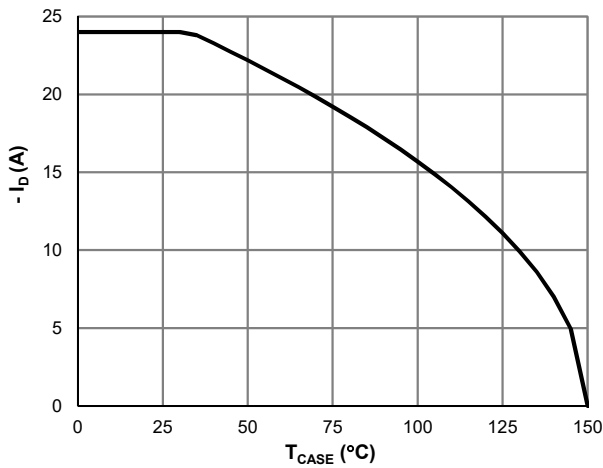
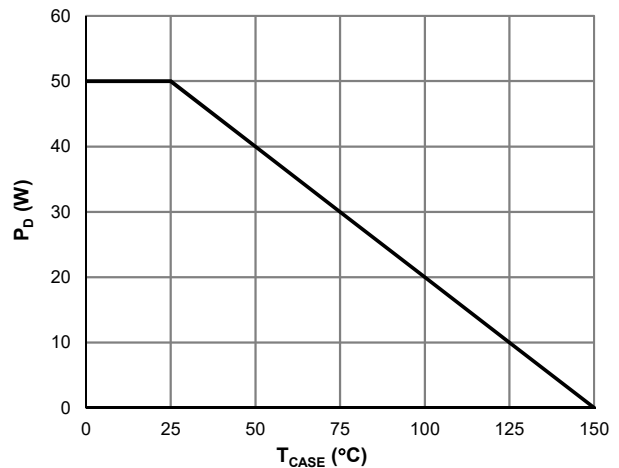
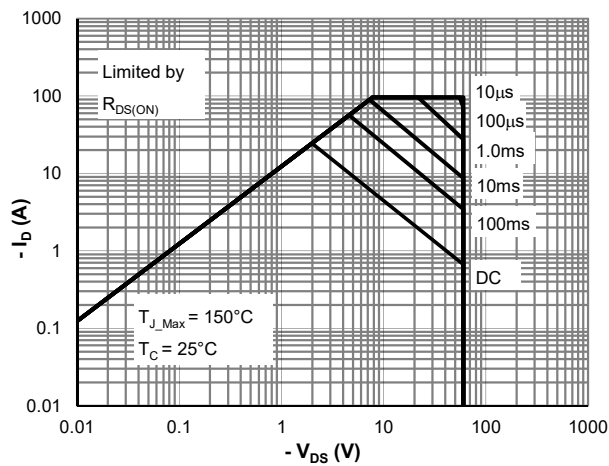
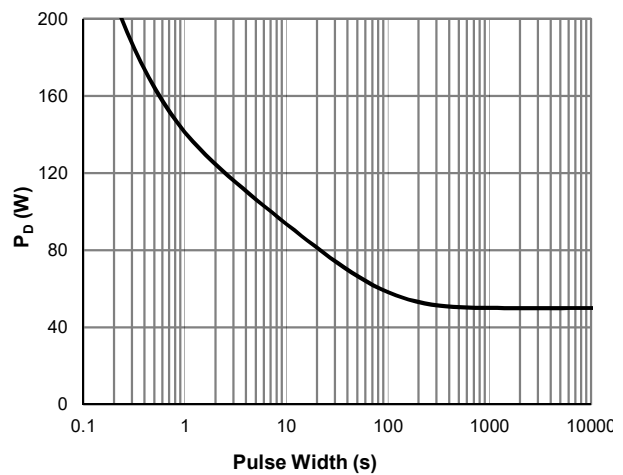
**Thermal Performance**

Parameter	Symbol	Typ.	Max.	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	60	69	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	2.5	2.9	$^\circ\text{C}/\text{W}$

**Notes:**

1. Computed continuous current assumes the condition of  $T_{J\_Max}$  while the actual continuous current depends on the thermal & electro-mechanical application board design.
2. This single-pulse measurement was taken under  $T_{J\_Max} = 150^\circ\text{C}$ .
3.  $E_{AS}$  of 75 mJ is based on starting  $T_J = 25^\circ\text{C}, L = 3.0\text{mH}, I_{AS} = -7.1\text{A}, V_{GS} = -10\text{V}, V_{DD} = -30\text{V}; 100\%$  test at  $L = 0.1\text{mH}, I_{AS} = -25\text{A}, T_{J\_Max} = 150^\circ\text{C}$ .
4. The power dissipation  $P_D$  is based on  $T_{J\_Max} = 150^\circ\text{C}$ .
5. This value is guaranteed by design hence it is not included in the production test.

**Typical Electrical & Thermal Characteristics**

**Figure 1: Saturation Characteristics**

**Figure 2: Transfer Characteristics**

**Figure 3:  $R_{DS(ON)}$  vs. Drain Current**

**Figure 4:  $R_{DS(ON)}$  vs. Junction Temperature**

**Figure 5:  $V_{GS(th)}$  vs. Junction Temperature**

**Figure 6:  $V_{BR(DSS)}$  vs. Junction Temperature**

**Typical Electrical & Thermal Characteristics**

**Figure 7: Body-Diode Characteristics**

**Figure 8: Capacitance Characteristics**

**Figure 9: Current De-rating**

**Figure 10: Power De-rating**

**Figure 11: Maximum Safe Operating Area**

**Figure 12: Single Pulse Power Rating, Junction-to-Case**



### Typical Electrical & Thermal Characteristics

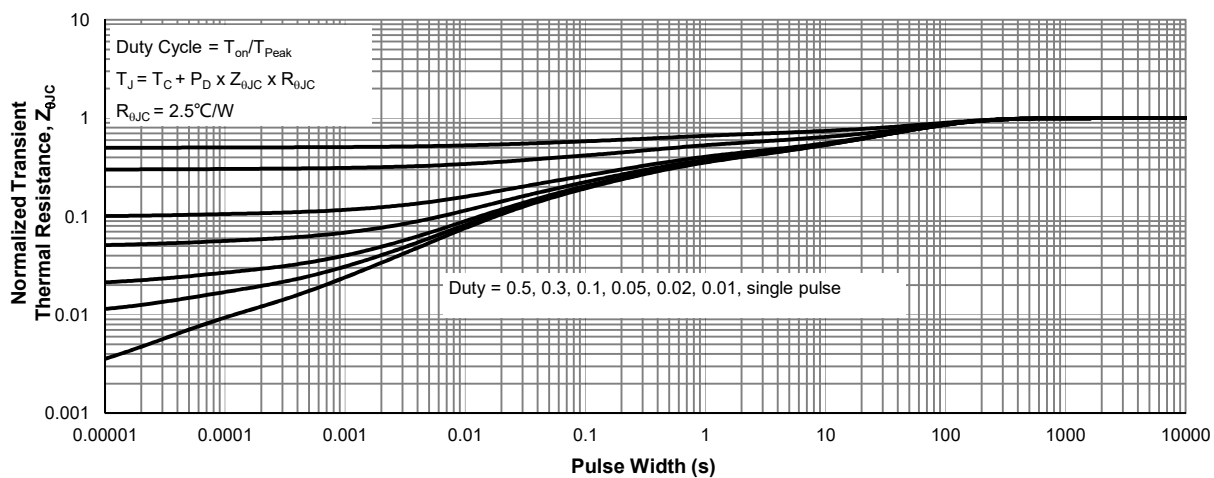
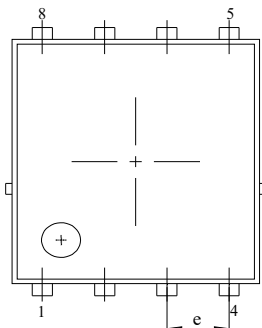
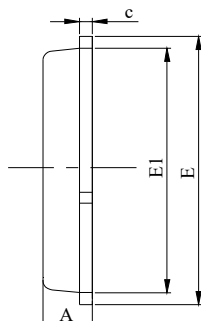


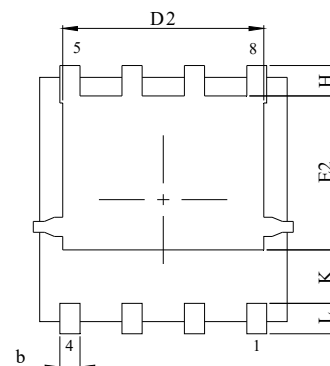
Figure 13: Normalized Maximum Transient Thermal Impedance

**PDFN5x6-8L Package Information**
**Package Outline**


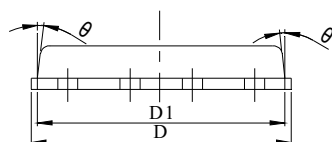
Top View



Side View



Bottom View

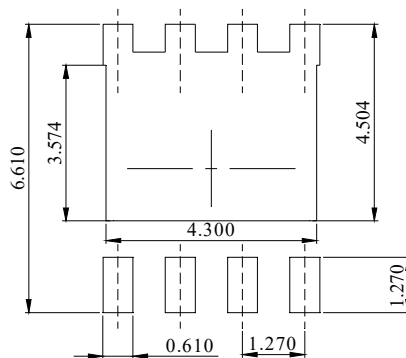


Front View

**NOTES:**

1. Dimension and tolerance per ASME Y14.5M, 1994.
2. All dimensions in millimeter (angle in degree).
3. Dimensions D1 and E1 do not include mold flash protrusions or gate burrs.

DIM.	MILLIMETER		
	MIN.	NOM.	MAX.
A	0.90	1.00	1.10
b	0.31	0.41	0.51
c	0.20	0.25	0.30
D	5.00	5.20	5.40
D1	4.95	5.05	5.15
D2	4.00	4.10	4.20
E	6.05	6.15	6.25
E1	5.50	5.60	5.70
E2	3.42	3.53	3.63
e	1.27BSC		
H	0.60	0.70	0.80
L	0.50	0.70	0.80
K	1.23 REF		
θ	-	-	10°

**Recommended Soldering Footprint**


DIMENSIONS:MILLIMETERS